AMENDMENTS TO THE CLAIMS

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Claims 1-29 are pending.

Please amend claims 1, 4, 6, 8, 15, 19-20, 22, and 26-27

Kindly cancel claims 7, 14, and 21, without prejudice.

No claims are added.

Accordingly, claims 1-6, 8-13, 15-20, and 22-29 remain pending.

The following listing of claims replaces all prior versions, and listings of claims in the application.

1. (Currently amended) A method for providing thread scheduling in a device, the device comprising one or more hardware elements operatively coupled to an operating system comprising a plurality of program modules, the method comprising:

scheduling one or more threads according to a predetermined periodic rate;

determining whether or not there are any threads to execute; and responsive to a determination that there are no threads to execute, deactivating at least one subset of components for a dynamic variable amount of time, the one subset being selected from a group of components comprising one or more of the hardware elements and the program modules for a dynamic variable amount of time, the dynamic variable amount of time being independent of the predetermined periodic rate and being based on a sleep state of a set of threads in a sleep queue.

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- 2. (Original) A method as recited in claim 1, wherein the dynamic variable amount of time is based on a maximum amount of time that a thread can yield before needing to be scheduled for execution.
- 3. (Original) A method as recited in claim 1, wherein the device is a battery powered device.
- 4. (Currently amended) A method as recited in claim 1, wherein the operating system comprises an operating system selected from a group of operating systems comprising is a Microsoft WINDOWS CE, Linux, WindRiver, QNX, or PALM operating system systems.
- (Previously presented) A method as recited in claim 1,
 wherein the predetermined periodic rate is a millisecond.
 - 6. (Currently amended) A method as recited in claim 1:

wherein the providing further comprises setting a system timer to generate a notification at the predetermined periodic rate;

wherein the deactivating further comprises resetting the system timer to generate the notification after the dynamic variable amount of time has elapsed since the deactivating; and

wherein the method further comprises:

receiving the notification after the dynamic variable amount of time has elapsed since the deactivating; and

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resetting the system timer to generate the notification at the predetermined periodic rate; and

activating the at least one subset of components the one or more of the hardware modules and the program modules.

- 7. (Canceled).
- 8. (Currently amended) A method for providing thread scheduling in a device, the device comprising one or more hardware elements operatively coupled to an operating system comprising a plurality of program modules, the method comprising:

scheduling one or more threads at a predetermined periodic rate; determining whether or not there are any threads to execute;

responsive to a determination that there are no threads to execute, deactivating at least one subset of components for a dynamic variable amount of time, the one subset being selected from a group of components comprising one or more of the hardware elements and the program modules for a dynamic variable amount of time, the dynamic variable amount of time being based on a sleep state of a set of threads in a sleep queue and independent of the predetermined periodic rate; and

activating the one subset of components one or more of the hardware elements and the program modules only when the operating system needs to perform an action selected from a group of actions comprising scheduling a thread for execution upon expiration of the dynamic variable

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amount of time since the deactivating, or upon receipt of an external event that is not a system timer event.

- 9. (Original) A method as recited in claim 8, wherein the device comprises a battery powered device.
- 10. (Original) A method as recited in claim 8, wherein the operating system comprises a Microsoft WINDOWS CE operating system.
- 11. (Previously presented) A method as recited in claim 8, wherein the predetermined periodic rate is a millisecond.

12. (Original) A method as recited in claim 8:

wherein the scheduling further comprises setting a system timer to the predetermined periodic rate, the predetermined periodic rate corresponding to a thread scheduling accuracy; and

wherein the deactivating further comprises resetting the system timer to generate a notification after the dynamic variable amount of time has elapsed since the deactivating.

13. (Original) A method as recited in claim 8:

wherein the deactivating further comprises resetting a system timer to generate a notification after the dynamic variable amount of time has elapsed, the dynamic variable amount of time being a maximum amount of

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wherein the activating further comprises resetting the system timer to the predetermined periodic rate to provide substantial thread scheduling accuracy.

14. (Canceled).

15. (Currently amended) A computer-readable storage medium containing computer-executable instructions for scheduling threads in a device, the device including an operating system comprised of a plurality of program modules that are in turn coupled to one or more hardware elements, the computer-executable instructions comprising instructions for:

determining at a periodic rate whether or not there are any threads to execute; and

responsive to a determination that there are no threads to execute, deactivating at least one subset of components for a dynamic variable amount of time, the at least one subset being selected from a group of components comprising the one or more of the program modules and one or more of the hardware elements for a dynamic variable amount of time, the dynamic variable amount of time being independent of the periodic rate, the dynamic variable amount of time being based on a sleep state of a set of threads in a sleep queue.

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- 16. (Original) A computer-readable storage medium as recited in claim 15, wherein the dynamic variable amount of time comprises a maximum amount of time that a thread has specified that it will yield to other threads before it needs to be scheduled for execution.
- 17. (Original) A computer-readable storage medium as recited in claim 15, wherein the device comprises a battery powered device.
- 18. (Original) A computer-readable storage medium as recited in claim 15, wherein the operating system comprises a Microsoft WINDOWS CE operating system.
- 19. (Currently amended) A computer-readable storage medium as recited in claim 15, wherein the computer-executable instructions further comprise instructions for:

in the deactivating, configuring a system timer to send a first timer interrupt after the dynamic variable amount of time has elapsed, the dynamic variable amount of time being a maximum amount of time that a first thread can yield to a second thread before the first thread needs to be executed; and

responsive to receiving the first timer interrupt:

(a) configuring the system timer to send a second timer interrupt at the periodic rate; and

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	(b) activating the deactivated at least one subset of components one
<u>)</u>	r more of the program modules and the hardware elements to determine if
h	nere are any threads to execute.

20. (Currently amended) A computer-readable storage medium as recited in claim 15, wherein the computer-executable instructions further comprise instructions for:

receiving an external interrupt before the dynamic variable amount of time has elapsed since the deactivating, the external interrupt not being a system timer interrupt; and

responsive to receiving the external interrupt, processing the external interrupt such that the at least one subset of components one or more of the program modules and the hardware elements remain deactivated for the dynamic variable amount of time.

21. (Canceled).

22. (Currently amended) A device comprising:

a processor;

a plurality of hardware elements coupled to the processor; and a memory coupled to the processor, the memory comprising computer-program instructions executable by the processor, the computer-program instructions comprising a scheduler program module, a hardware abstraction layer (HAL) program module, one or more operating system program modules, and a set of application program modules;

for:

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wherein the scheduler comprises computer-executable instructions

scheduling threads for execution at a periodic time interval; determining that there are no threads to execute; and

wherein the HAL, responsive to the determining, comprises computer-executable instructions for deactivating, for a dynamic variable amount of time, at least one subset of components selected from a group of components comprising one or more of the scheduler, the hardware elements, the one or more operating system program modules, and the application program modules, the dynamic variable amount of time being independent of the periodic time interval and being based on a sleep state of a set of threads in a sleep queue.

- 23. (Original) A device as recited in claim 22, wherein the dynamic variable amount of time is based on a maximum amount of time that a thread can yield before needing to be scheduled.
- 24. (Original) A device as recited in claim 22, wherein the periodic time interval is a millisecond.
- 25. (Original) A device as recited in claim 22, wherein the device comprises a battery powered device.
- 26. (Currently amended) A device as recited in claim 22, wherein the operating system is an operating system selected from a group of

 operating systems comprising a Microsoft WINDOWS CE, Linux, WindRiver, QNX, or Palm ® operating system systems.

- 27. (Currently amended) A device as recited in claim 22, wherein the HAL further comprises computer-executable instructions for reactivating the at least one subset of components after the dynamic variable amount of time has elapsed since the at least one subset of components one or more of the program modules and the hardware elements were deactivated.
- 28. (Original) A device as recited in claim 27, wherein the scheduler is re-activated in a manner that allows the scheduler to schedule threads based on the periodic time interval.
- 29. (Original) A device as recited in claim 22, wherein after the scheduler is deactivated, the HAL further comprises computer-executable instructions for receiving a notification in response to an external event, the external event not being a system timer event, responsive to receipt of the notification, the HAL processing the notification in a manner that the scheduler remains deactivated for the dynamic variable amount of time.